

Camera Link FMC Module

Overview

The FMC-CL is a Camera Link interface module designed as a FMC card with LPC connector. The module supports all configurations of Camera Link interface (Base, Medium, Full and Extended-Full) or two separate Base Camera Link interfaces. Power over Camera Link (PoCL) with the power management and monitoring is also supported. Camera Link IP-core for the Xilinx FPGA is provided on demand.*

* Requires Xilinx 7 family FPGA, available IP core for Vivado

Typical Applications

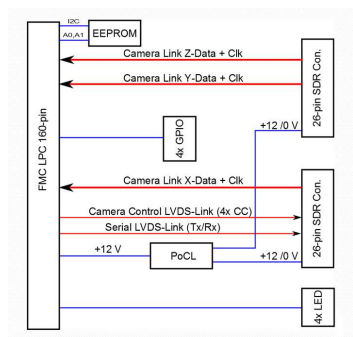
- Image Acquisition and Image Processing Systems
- Video Processing Systems
- High Energy Physics
- Medical Applications
- 3D Applications
- 3D Tomography
- Research & Development
- Image Analysis

Features

- FMC Vita 57.1 Compliant, Low Pin Count Connector
- Base, Medium, Full, Extended and Dual Base Configurations of Camera Link
- PoCL v1.2 support
- Acquisition pixel clock rates up to 85 MHz
- Supports all Camera Link bit assignment configurations
- Temperature Range: 0°C to 55°C
- Air Cooled
- FMC Vadj = 2.5 V, 1 A max.
- FMC 12V0P 800 mA (~400 mA per camera PoCL output, dual config)

Front panel interfaces:

- Dual Camera Link interface
- 4 LED indicators



Block diagram of the MFMC module

AIES offers modification of this product according to customer specification.

Ordering information	
Part Number	Description
FMC-CL	Two independent Camera Link connectors with PoCL, RoHS Compliant